

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chung-Kuan Cheng

Serial No.: 10/526,523

Conf. No.: 5310

Filed: March 1, 2005

For: INTERCONNECTION ARCHITECTURE
AND METHOD OF ASSESSING
INTERCONNECTION
ARCHITECTURE

Art Unit: 2818

Examiner: Nguyen, Thanh T.



I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

Jan. 9, 2007
Date

Arik B. Ranson
Registration No. 43,874
Attorney for Applicant

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Election/Restriction Requirement mailed November 9, 2006, Applicants elect Species I, claims 1-8 and 19, directed to a chip, without traverse. Applicants reserve the right to timely file divisional applications directed at least to the non-elected claims.

If outstanding issues exist, the Examiner is respectfully requested to contact Applicants' attorney at the number listed below.

Respectfully submitted,

Customer No. 24978

January 9, 2007

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GREER, BURNS & CRAIN, LTD.

By

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